Applicant(s): A. Satoh et al. FORM PTO-1449 (MODIFIED) Docket No.: JP920020242US1 LIST OF PUBLICATIONS FOR Serial No.: To Be Assigned APPLICANT'S INFORMATION Filing Date: January 21, 2004 To Be Assigned Group: DISCLOSURE STATEMENT **U.S. PATENT DOCUMENTS** FILING DATE **EXAMINER** INITIAL DOCUMENT NO. DATE NAME CLASS/SUBCLASS IF APPROPRIATE FOREIGN PATENT DOCUMENTS TRANSLATION **EXAMINER** DATE COUNTRY CLASS/SUBCLASS INITIAL DOCUMENT NO. **OTHER DOCUMENTS EXAMINER** INITIAL REF NO. AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC 1. Johann Großschädl, "A Bit-Serial Unified Multiplier Architecture for Finite Fields GF (p) and GF (2")," /TM/ CHES 2001, LNCS 2162, pp. 202-219, 2001.

/Tan Mai/

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